

HP-41C PPC ROM Memory to Stack by Richard Nelson  
PPC Calculator Journal Vol 7 No 10 Pg 9 Dec 1980

```
01 LBL "MS" ;PPC ROM Memory to Stack Routine
02 4
03 ST+ 00
04 RCL IND 00
05 SIGN
06 DSE 00
07 RCL IND 00
08 DSE 00
09 RCL IND 00
10 DSE 00
11 RCL IND 00
12 DSE 00
13 RCL IND 00
14 RTN
15 END
```

0020C000F3004D5314920090807A9700908097009080970090  
809700908085C0000D91

32 BYTES

HP-41C PPC ROM PPC Logo V1 by Richard Nelson  
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```
01 LBL "PPC" ;PPC ROM Copyright Version 1
02 CF 21
03 "MEMORY LOST"
04 AVIEW
05 PSE
06 "JUST KIDDING"
07 AVIEW
08 PSE
09 " C. PPC 1981"
10 AVIEW
11 STOP
12 END

0038C000F400505043A915FB4D454D4F5259204C4F53547E89
FC4A555354204B4944444494E477E89FC20432E205050432031
3938317E84C0000D7F
```

56 BYTES

HP-41C PPC ROM PPC Logo V2 by Richard Nelson  
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```
01 LBL "PPC" ;PPC ROM Copyright Version 2
02 CF 21
03 "MEMORY LOST"
04 AVIEW
05 PSE
06 "JUST KIDDING"
07 AVIEW
08 PSE
09 FC? 55
10 GTO 00
11 SF 21
12 9
13 STO 06
14 STO 07
15 .
16 STO 08
17 XEQ "SC" ;Special Characters (V7N10P14 SC Line 1)
18 SF 12
19 RCL 02
20 XROM 29,04 ;ACSPEC
21 7
```

```

22 XROM 29,23 ;SKPCOL
23 XEQ "LG" ;PPC Logo (V7N10P18 LG Line 1)
24 7
25 XROM 29,23 ;SKPCOL
26 "1981"
27 XROM 29,01 ;ACA
28 XROM 29,10 ;PRBUF
29 RTN
30 LBL 00
31 " C. PPC 1981"
32 AVIEW
33 RTN
34 END

```

```

0061C000F400505043A915FB4D454D4F5259204C4F53547E89
FC4A555354204B494444494E477E89AD37B100A8151936371A
381EF25343A80C22A74417A7571EF24C4717A757F431393831
A741A74A8501FC20432E2050504320313938317E85C0000D2D

```

97 BYTES

HP-41C PPC ROM Block Operations by Various Authors  
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```

01 LBL "BC" ;PPC ROM Block Clear Routine
02 FC? 10
03 .
04 FS?C 10
05 X<>Y
06 LBL 01
07 STO IND Y
08 ISG Y
09 GTO 01
10 RTN
11 LBL "BE" ;PPC ROM Block Exchange Routine
12 SIGN
13 LBL 02
14 RCL IND Z
15 X<> IND Z
16 STO IND T
17 RDN
18 ST+ Z
19 ST+ Y
20 DSE L
21 GTO 02
22 RTN
23 LBL "BI" ;PPC ROM Block Increment Routine
24 E-3
25 LBL 03
26 STO IND Y
27 E-3
28 +
29 ISG Y
30 GTO 03
31 RTN
32 LBL "BM" ;PPC ROM Block Move Routine
33 STO L
34 RDN
35 X>Y?
36 GTO 04
37 1
38 GTO 05
39 LBL 04
40 R^
41 ST+ Z
42 +
43 -1
44 ST+ Z
45 ST+ Y
46 LBL 05

```

```

47 RCL IND Z
48 STO IND Z
49 RDN
50 ST+ Z
51 ST+ Y
52 DSE L
53 GTO 05
54 RTN
55 LBL "BP" ;PPC ROM Block Plus Routine
56 .
57 LBL 06
58 RCL IND 00
59 FS? 10
60 S+ ;Sigma+
61 FS? 10
62 CLX
63 FC? 10
64 +
65 ISG 00
66 GTO 06
67 STO IND 00
68 RTN
69 LBL "BR" ;PPC ROM Block Rotate Routine
70 RCL 00
71 ABS
72 CLX
73 RCL IND L
74 LBL 07
75 ISG L
76 GTO 08
77 GTO IND 00
78 RTN
79 LBL 08
80 X<> IND L
81 GTO 07
82 RTN
83 END

```

```

00A0C000F3004243AD0A1AAA0A710291F29672B20085C000F3
0042457A0390F1CEF191F075927192729774B30085C000F300
42491B1C130491F21B1C13409672B40085C000F300424D9174
7545B50011B60005749271401C11927192720690F191F17592
7192729774B60085C000F30042501A079080AC0A47AC0A77AD
0A409600B700918085C000F300425220617790F4089674B900
AE008509CEF4B80085C0000DEB

```

160 BYTES

HP-41C PPC ROM Stack to Memory by Richard Nelson  
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```

01 LBL "SM" ;PPC ROM Stack to Memory Routine
02 XEQ c
03 XEQ c
04 XEQ c
05 XEQ c
06 LASTX
07 STO IND 00
08 4
09 ST- 00
10 RTN
11 LBL c
12 STO IND 00
13 CLX
14 1
15 ST+ 00
16 RDN
17 RTN
18 END

```

HP-41C PPC ROM Stack to Memory by Richard Nelson  
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0026C000F300534DE0007DE0007DE0007DE0007D7691801493  
0085CF7D9180771192007585C0000DDE

38 BYTES

HP-41C PPC ROM XROM Inputs for LB by Roger Hill  
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```
01 LBL "XI" ;PPC ROM XROM Inputs for LB
02 INT
03 RCL X
04 LASTX
05 FRC
06 E2
07 *
08 X<>Y
09 4
10 MOD
11 64
12 *
13 +
14 X<>Y
15 4
16 /
17 INT
18 160
19 +
20 RTN
21 END
```

0021C000F300584968907376691B124271144B161442407114  
43681116104085C0000DA3

33 BYTES

HP-41C PPC ROM Memory->Stack by Richard Nelson PPC V7 N10 P9 Dec 1980

Program Registers Needed: 5

Row 1 (1 - 6)



Row 2 (6 - 12)



Row 3 (13 - 15)



# HP-41C PPC ROM PPC Logo 1 by Richard Nelson PPC V7 N10 P9 Dec 1980

Program Registers Needed: 8

Row 1 (1 - 3)



Row 2 (3 - 6)



Row 3 (6 - 9)



Row 4 (9 - 10)



Row 5 (11 - 12)



# HP-41C PPC ROM PPC Logo 2 by Richard Nelson PPC V7 N10 P9 Dec 1980

Program Registers Needed: 14

Row 1 (1 - 3)



Row 2 (3 - 6)



Row 3 (6 - 9)



Row 4 (9 - 17)



Row 5 (17 - 23)



Row 6 (24 - 29)



Row 7 (30 - 31)



Row 8 (31 - 34)



# HP-41C PPC ROM Block Ops by Various Authors PPC V7 N10 P9 Dec 1980

Program Registers Needed: 23

Row 1 (1 - 6)



Row 2 (7 - 11)



Row 3 (12 - 19)



Row 4 (20 - 24)



Row 5 (24 - 31)



Row 6 (32 - 37)



Row 7 (38 - 45)



Row 8 (46 - 53)



Row 9 (53 - 59)



Row 10 (59 - 67)



Row 11 (67 - 73)



Row 12 (74 - 81)



Row 13 (82 - 83)



# HP-41C PPC ROM Stack->Memory by Richard Nelson PPC V7 N10 P9 Dec 1980

Program Registers Needed: 6

Row 1 (1 - 4)



Row 2 (4 - 11)



Row 3 (11 - 18)



# HP-41C PPC ROM XROM Inputs for LB by Roger Hill PPC V7 N10 P9 Dec 1980

Program Registers Needed: 5

Row 1 (1 - 6)



Row 2 (7 - 18)



Row 3 (18 - 21)

